TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

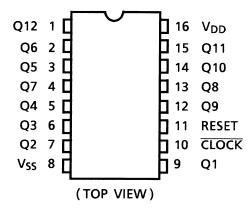
TC4040BP,TC4040BF,TC4040BFN

TC4040B 12 Stage Ripple-Carry Binary Counter/Dividers

TC4040B is 12 stage ripple carry binary counter having asynchronous clear function. This counter advances its counting stage by falling edge of \overline{CLOCK} input. When RESET input is placed "H", all the circuits are reset regardless of \overline{CLOCK} input making all the outputs (Q1 through Q12) to be "L".

This is most suitable for frequency dividers, control circuits and timing circuits.

Pin Assignment



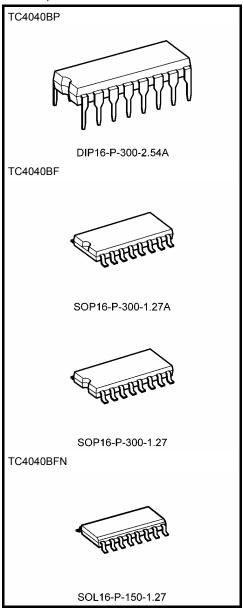
Truth Table

$\overline{CLOCK}\ \Delta$	RESET	Output State		
*	* H All Outp			
	L	No Change		
\Box	L	Advance to Next State		

Δ: Level change

*: Don't care

Note: xxxFN (JEDEC SOP) is not available in Japan.

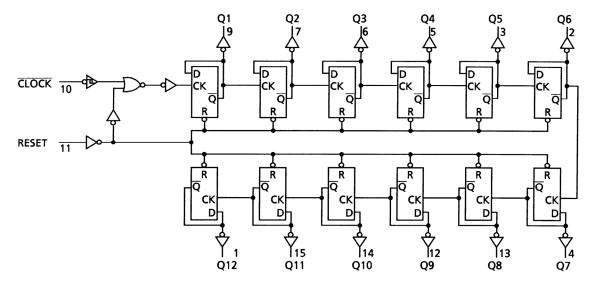


Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOP16-P-300-1.27 : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)



Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Input voltage	V _{IN}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	٧
Output voltage	V _{OUT}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	٧
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Recommended Operating Conditions (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V_{IN}		0	_	V_{DD}	V

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.



Static Electrical Characteristics ($V_{SS} = 0 V$)

(haracteristics)		Sym-	Test Condition		-40°C		25°C			85°C		
		bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output voltage			$ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5	4.95	_	4.95	5.00	_	4.95	_	
		V_{OH}		10	9.95	_	9.95	10.00	_	9.95	_	V
Ū			VIN - VSS, VDD	15	14.95	_	14.95	15.00	_	14.95	_	
			 I _{OUT} < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05	
Low-level voltage	output	V_{OL}	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05	_	0.05	V
			VIN - VSS, VDD	15	_	0.05	_	0.00	0.05	_	0.05	
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V _{OH} = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA
Output hig	h current	loh	V _{OH} = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	
			V _{OH} = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
			V _{OL} = 0.4 V	5	0.61	_	0.51	1.5	_	0.42	_	
Output lov	v current	I _{OL}	V _{OL} = 0.5 V	10	1.50	_	1.30	3.2	_	1.10	_	mA
Output lov	v current		V _{OL} = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		V _{IH}	V _{OUT} = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	V
Input high	voltago		V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
input nign	voitage		V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	
			$ I_{OUT} < 1 \mu A$									
			V _{OUT} = 0.5 V, 4.5 V	5		1.5	_	2.25	1.5	_	1.5	
Input low voltage		V _{IL}	V _{OUT} = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V
			V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	
			$ I_{OUT} < 1 \mu A$									
Input	"H" level	I _{IH}	V _{IH} = 18 V	18	_	0.1	_	10 ⁻⁵	0.1	_	1.0	μА
current	"L" level	I _{IL}	V _{IL} = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ
			V V V	5	_	5	_	0.005	5	_	150	
Quiescent current	Quiescent supply current		$V_{IN} = V_{SS}, V_{DD}$	10	_	10	_	0.010	10	_	300	μА
			(Note)	15	_	20	—	0.015	20	—	600	

3

Note: All valid input combinations.



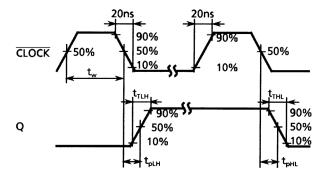
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Ohamadadaha	O mala al	Test Condition	N 41:	т		Linit	
Characteristics	Symbol		V _{DD} (V)	Min	Тур.	Max	Unit
Output transition time			5	_	70	200	
Output transition time	tтLH	_	10	_	35	100	ns
(low to high)			15	_	30	80	
Output transition time			5	_	70	200	
Output transition time	t _{THL}	_	10	_	35	100	ns
(high to low)			15	_	30	80	
Dranagation delay time			5	_	160	360	
Propagation delay time (CLOCK -Q1)	t _{pLH}	_	10	_	80	160	ns
(CLOCK -Q1)			15	_	65	130	
Draw a setion date: time			5	_	160	360	
Propagation delay time (CLOCK -Q1)	t _{pHL}	_	10	_	80	160	ns
(CLOCK -QT)			15	_	65	130	
Propagation delay time			5	_	900	1800	
(CLOCK -Q12)	t _{pLH}	_	10	_	450	900	ns
(CLOCK -Q12)			15	_	360	720	
December delevitime			5	_	900	1800	
Propagation delay time (CLOCK -Q12)	t _{pHL}	_	10	_	450	900	ns
(CLOCK -Q12)			15	_	360	720	
Dranagation delay time			5	_	150	280	
Propagation delay time	t _{pHL}	_	10	_	70	120	ns
(RESET-Q)			15	_	50	100	
			5	3.5	10	_	
Max clock frequency	f _{CL}	_	10	8.0	20	_	MHz
			15	12.0	25	_	
			5	_	50	140	
Min clock pulse width	t _W	_	10	_	20	60	ns
			15	_	15	40	
Min pulso width			5	_	100	200	
Min pulse width	t _W	_	10	_	40	80	ns
(RESET)			15	_	30	60	
Min removal time			5	_	_	350	
(RESET- CLOCK)	t _{rem}	_	10	_	_	150	ns
(NESET-GLOOK)			15		_	100	
Max clock input rise time	+ =:		5				
Max clock input rise time Max clock input fall time	trou	_	10	No limit			μS
IVIAA GOOK IIIPULIAII LIITIE	t _{fCL}		15				
Input capacitance	C _{IN}			_	5	7.5	pF

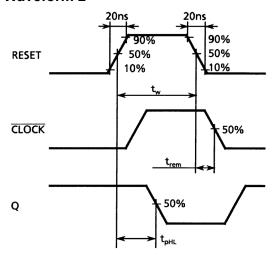


Waveforms for Measurement of Dynamic Characteristics

Waveform 1

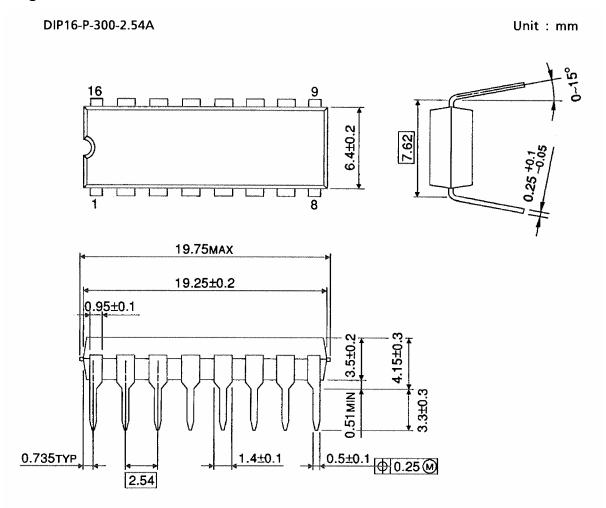


Waveform 2





Package Dimensions

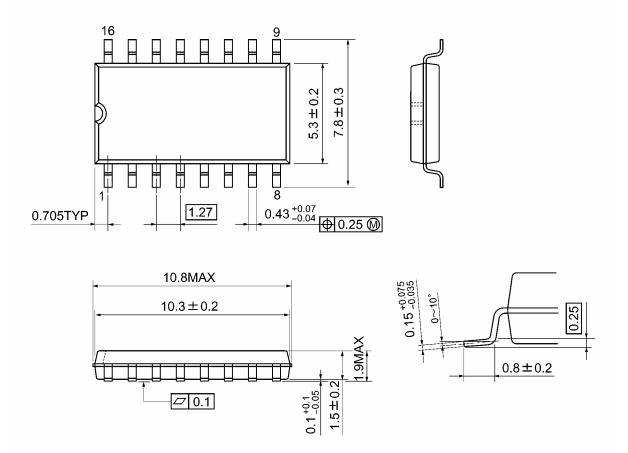


6

Weight: 1.00 g (typ.)

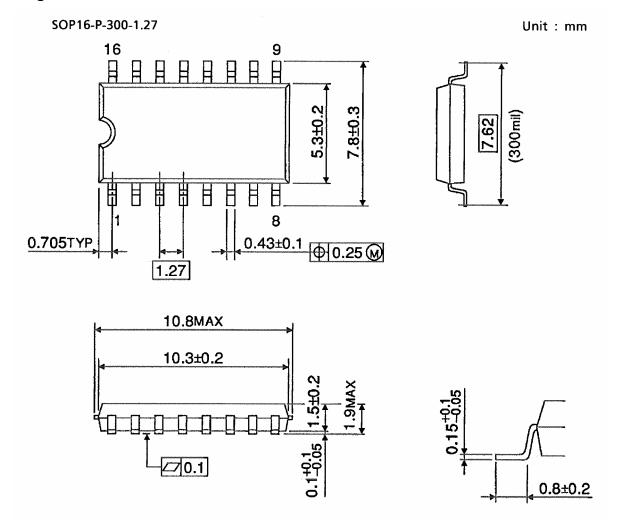
Package Dimensions

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions

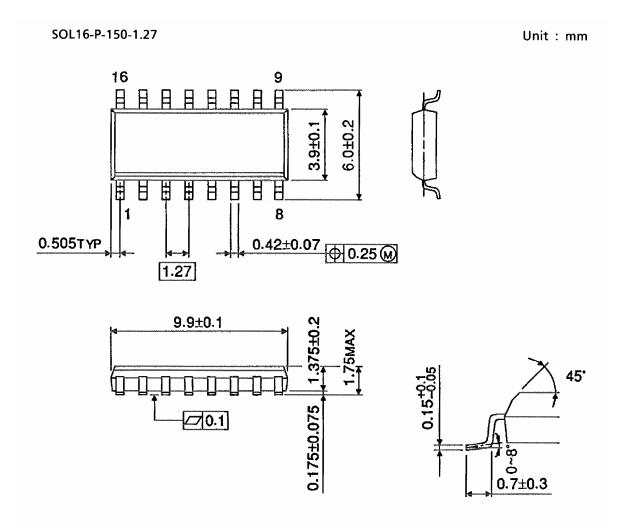


8

Weight: 0.18 g (typ.)



Package Dimensions (Note)



9

Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

Note: Lead (Pb)-Free Packages

DIP16-P-300-2.54A SOP16-P-300-1.27A SOL16-P-150-1.27

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